AP

<u>S/N 09/893,527</u> <u>PATENT</u>

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Shmuel Wimer

Examiner: Dwin M Craig

Serial No.:

09/893,527

Group Art Unit: 2123

Filed:

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Docket No.: 80107.072US1

Title:

ARRANGEMENTS FOR AUTOMATIC RE-LEGGING OF TRANSISTORS

P Assignee:

JUL 1 4 2005

Intel Corporation

AMENDMENT AND RESPONSE UNDER 37 CFR § 1.116

Astri Stop AF

Commissioner for Patents

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This responds to the Final Office Action mailed on <u>June 30, 2005</u>. Please amend the above-identified patent application as follows.

9-6-2005 Please onter